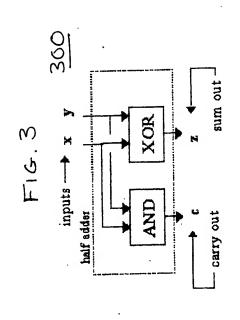


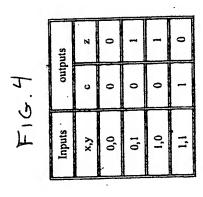
FIG. 2



c\_low| c HA

F16.5

full adder



|   | uts     | anm      | 0     | -     | -     | 0     | -     | 0     | 0     | _     |
|---|---------|----------|-------|-------|-------|-------|-------|-------|-------|-------|
|   | outputs | c_out    | 0     | 0     | 0     | -     | 0     | ~-    | -     | -     |
| • | Inputs  | c_in,a,b | 0,0,0 | 0,0,1 | 0,1,0 | 0,1,1 | 1,0,0 | 1,0,1 | 1,1,0 | 1,1,1 |
| ! | .·      |          | -     | •     |       |       |       |       |       |       |

F16.6

sum out --- sum

carry out

